

**REMARKS****Additional Remarks concerning the Patentability of Claims 1-41 over the Various Heitz Et Al., Heikman et al. and Cuomo et al. References Cited in the §102 and §103 Rejections of Claims 1-41**

In the November 30, 2004 Office Action, claims 1-41 were rejected on reference grounds, including:

- a rejection of claims 1-10 under 35 USC §102(b) over Heitz et al.;
- a rejection of claims 11-13, 15-29, 31-38 under 35 USC §103(a) over Heitz et al.;
- a rejection of claim 30 under 35 USC §103(a) over Heitz et al. in view of Heikman et al.; and
- a rejection of claims 14 and 39-41 under 35 USC §103(a) over Heitz et al. in view of Cuomo et al.

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Concerning the rejection of claims 1-10 on §102 grounds, based on Heitz et al., claim 1, from which claims 2-10 depend, recites:

**"Large-area, single-crystal semi-insulating gallium nitride."**

The term "large-area" is defined in the description of the present application in paragraph [0022], at page 6:

"As used herein, the term 'large area' in reference to the GaN material means that such material has a diameter of at least 25 millimeters, or in the case of square or rectangular wafers, a diagonal dimension of at least 25 millimeters. The thickness dimension desirably is at least 300 micrometers, e.g., a thickness in a range of from about 300 micrometers to about 5 centimeters or more. These dimensions are in reference to the wafers as formed from the original crystal growth single wafers or from boules by steps including initial crystal growth to form the boule or ingot article, followed by rounding, sizing, slicing, lapping, polishing, etc. as necessary

to produce wafers having surfaces suitable for epitaxial growth thereon."

As related in the background section of the present application, such "large area" single crystal, insulating gallium nitride has not been achieved by the prior art:

"[0005] U.S. Patent 6,273,948 issued to Porowski et al. describes a method of fabricating highly resistive GaN bulk crystals, by crystallization from a solution of atomic nitrogen in a molten mixture of gallium and Group II metal such as beryllium or calcium, under high pressure of 0.5-2.0 GPa and high temperature of 1300-1700° C. Resistivity of  $1 \times 10^4$  to  $1 \times 10^8$  ohm-centimeter (ohm-cm) was achieved. However, the crystal obtained from the process was about 1 cm in size, whereas most commercial electronic applications require a substrate size of at least about 2 inches (> 5 cm) diameter.

"[0006] U.S. Patent 5,686,738 (Moustakas), U.S. Patent 6,544,867 (Webb et al.), U.S. Patent 6,261,931 (Keller et al.) and U.S. Patent Application 2002/0096692 A1 (Nakamura et al.), disclose various methods of making semi-insulating GaN films on a foreign substrate. All of these approaches are susceptible to TE mismatch issues, and the resultant Boeing, cracking and small feature fabrication difficulties discussed above, and none of such approaches has yielded a commercially viable, large-area single-crystal semi-insulating gallium nitride material."

The foregoing references of the above-quoted background text of the application are of interest, since the references identified in such passage are all directed to formation of high resistivity GaN, and all of these references were published after the February 15, 1997 publication date of Heitz et al. (Porowski published on December 10, 1998; Moustakas was issued November 11, 1997; Webb et al. issued on April 8, 2003; Keller et al. issued on July 17, 2001; and Nakamura et al. was published on July 25, 2002), yet none of the authors of these references was able to make a large area, semi-insulating, single-crystal GaN material, and these references underscore the fact that large-area, semi-insulating, single-crystal GaN material likewise is not disclosed or suggested by Heitz et al.

Heitz et al. describes GaN layers that are epitaxially grown on (0001) sapphire, but there is no disclosure or any suggestion whatsoever of the size of the crystal, merely a mention that the semi-insulating GaN crystal was 38  $\mu\text{m}$  thick (since, as disclosed in paragraph [0031] of the present application, "[T]he thickness dimension [of applicants' invention] desirably is at least 300

micrometers," this is hardly suggestive of any material of the present invention). Additionally, there is no disclosure in Heitz et al. that identifies the GaN material of such reference as being single crystal in character.

Instead, the GaN material of the Heitz et al. reference is ambiguously and indefinitely characterized. There is no disclosure of bulk resistivity of the GaN material in Heitz et al. Heitz et al. instead is focused on photoluminescence excitation results for iron (3+) luminescence in hexagonal GaN, but even here, Heitz et al. refers to crystal material as having semi-insulating character on the basis of an electron paramagnetic resonance (EPR) signal that is observable in the dark (see Heitz et al. at the page 4382, second column, last paragraph ("[T]he Fe<sup>3+</sup> EPR signal can be observed in the dark for the crystals 2 and 3 *confirming the semi-insulating character of these two samples*"-emphasis added)).

In the sentence bridging pages 4382 and 4383 of the article, and in the following sentence on page 4383 of the article, Heitz et al. state that in addition to the luminescence observed attributable to the tri-positive cation of iron, luminescence was attributed to Cr<sup>4+</sup> and Ti<sup>2+</sup> as well.

In the results section of the article, on page 4383, the authors note that iron "is a general contamination of the crystals" used in their work, and that the luminescence attributed to Fe<sup>3+</sup> "is representative of all the samples investigated" (including crystal characterized as n-type).

The Heitz et al. paper ignores the basic standard that semi-insulators are characterized by bulk resistivity.

Heitz et al. contains no resistance measurements, no potential gradient determinations, and no rigorous basis for characterizing any GaN material as being semi-insulative in character.

Further, the luminescence that is the sole basis of the semi-insulative characterization, is attributed to general contamination of the crystals by iron, chromium and titanium, contaminants that are conceded by the authors to be present in all crystal material considered by them, including crystal that is characterized as n-type on the basis of its photoluminescence behavior.

Thus, there is no basis for one of ordinary skill in the art to attribute to Heitz et al. any achievement of semi-insulative GaN, in view of the indefinite and ambiguous disclosure of such reference, and the lack of any specific resistivity values therein.

Accordingly, there is no derivative basis in Heitz et al. for the large-area, single-crystal semi-insulating gallium nitride of applicants' invention that is recited in claim 1 of the present application.

As is well understood by those skilled in the art, semi-insulating material is characterized by resistivity. See, for example, paragraph [0024] of the present application:

[0024] As used herein, the term "semi-insulating" in reference to the semi-insulating GaN material of the invention means that such material has a resistivity > 100 ohm-centimeters ( $\Omega\text{-cm}$ ) at room temperature (~ 25°C). In one embodiment, the GaN material of the invention may have a resistivity >  $10^2 \Omega\text{-cm}$  at 200°C. In another embodiment, the semi-insulating GaN material may have a resistivity >  $10^5 \Omega\text{-cm}$  at room temperature. More preferably, the semi-insulating GaN material has a resistivity >  $10^5 \Omega\text{-cm}$  at 200°C, and most preferably the semi-insulating GaN material has a resistivity >  $10^5 \Omega\text{-cm}$  at 300°C. Such values of resistivity are determined by four point probing techniques (van der Pauw contact geometry) as a function of temperature. In instances where the GaN material, e.g., as a free-standing substrate article, has microelectronic circuitry fabricated on and/or within such substrate, the GaN material of the invention has a semi-insulating character in the operating temperature regime of such microelectronic circuitry. The term "within" in such context refers to circuitry in which the substrate forms a part of the device, e.g., wherein the substrate is subjected to an implantation process to form implanted device region(s) in the substrate.

As is apparent from the foregoing, the contention that Heitz et al. anticipates the subject matter of claims 1-10 is without substantive basis, since (1) Heitz et al. does not teach or suggest large-area gallium nitride, (2) Heitz et al. does not provide any rigorous determination of semi-insulating character since the photoluminescence characterization, a non-rigorous characterization, is based on the presence of the same contaminants as are present in GaN material identified as n-type, and no

resistivity information or data is given for any of the materials tested by the authors, and (3) there is no mention anywhere in Heitz et al. of "single crystal" GaN material.

Additional basis of distinction is present in claims 5-8, which recite manganese, cobalt, nickel and copper, respectively. There is no mention of such species in Heitz et al. as dopants for GaN.<sup>1</sup>

It is fundamental law that anticipation under §102 requires the presence in a single reference of each and every element of the claimed invention, arranged as in the claim. *Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co.*, 221 USPQ 481, 485 (Fed. Cir. 1984); *Verdegaal Bros. Inc. v. Union Oil Co.*, 814 F2d 628, 631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1986), cert. denied, 484 U.S. 827 (1987). In the present case, Heitz et al. does not disclose each and every element of applicants claimed invention, as recited in applicants' claims 1-10. The stated basis of rejection therefore cannot stand.

Accordingly, it is requested that the examiner reconsider the subject matter of claims 1-10 in light of the foregoing remarks, and correspondingly withdraw the rejection of claims 1-10 based on Heitz et al.

The same distinguishing remarks as set out above are apposite to the rejection of claims 11-13, 15-29 and 31-38 under §103 as being obvious over Heitz et al. It again is pointed out that (1) Heitz et al. does not teach or suggest large-area gallium nitride, (2) Heitz et al. does not provide any rigorous determination of semi-insulating character since the photoluminescence characterization, a non-rigorous characterization, is based on the presence of the same contaminants as are present in GaN material identified as n-type, and no resistivity information or data is given for any of the materials tested by the authors, and (3) there is no mention anywhere in Heitz et al. of "single crystal" GaN material. Accordingly, since claims 11-13, 15-29 and 31-38 require "large-area", "single crystal" and "semi-insulating" gallium nitride, such claims are patently distinguished over Heitz et al. and in condition for allowance.

Additional bases are present of patentable distinction in the claims. Heitz et al. teaches a "crystal 3" material that is 38 micrometers thick, but contains no teaching or suggestion of any higher thickness

<sup>1</sup> It is noted that Heitz, et al. discloses manganese, but in zinc sulfide, not GaN; nickel is discussed in connection with zinc sulfide and cadmium sulfide, but not in connection with GaN; copper is mentioned in connection with II-VI semiconductors, but not in connection with III-V semiconductors such as GaN.

of such "crystal 3" material. Accordingly, there is no derivative basis in Heitz et al. for a thickness in a range of from about 50 micrometers to about 5 cm, as recited in claim 11, or a thickness of from about 300 micrometers to about 5 cm, as recited in claim 13. The same is true for claim 15, which recites a thickness of at least 300 micrometers, and additionally has a diameter of at least 50 mm, for which there is no described or suggestive basis in Heitz et al., and it is also true for claim 16, reciting a thickness in a range of from 300 micrometers to 5 cm.

Further, the resistivity values recited in claim as 17-21 have no derivative basis in Heitz et al., since Heitz et al. fail to disclose any resistivity values, instead focusing on PLE spectra, with the accompanying ambiguity and indefinite issues discussed hereinabove. There is correspondingly no basis in Heitz et al. for the impurity concentrations recited in claim 22-25, the defect density levels recited in claims 26-28, or the silicon-oxygen-iron recital of claim 29.

Claims 31-38 likewise have no derivative basis in the Heitz et al. teachings; the gallium nitride recited in (dependent) claims 31-34 is gallium nitride according to claim 1, which Heitz et al. fails to disclose (see preceding discussion).

Additionally, Heitz et al. does not disclose fabrication of any microelectronic device or in the materials described in such reference - although there is mention in the background portion of the article of Group-III nitride-based device structures, there is no teaching or suggestion that the specific GaN material described by Heitz et al. is suitable for specific devices. Instead, the Heitz et al. article is simply a report on photoluminescence excitation investigations of the hexagonal GaN samples, and Heitz et al. contains no instructions or direction for fabricating any microelectronic or optoelectronic device on or in the material that is the subject of the authors' empirical studies. Heitz et al. thus provides no basis for the subject matter of claims 35-38, even if the fact is ignored that such claims are of dependent form under claim 1 and are thereby patentably differentiated over Heitz et al. on such basis alone.

The examiner in reference to Heitz et al. has contended (in paragraph 44, at page 7 of the Office Action) that it would have been obvious to determine optimum thickness, temperature and condition of delivery of layers involved. The relevance of this statement is not understood, since Heitz et al. provides no "starting basis" from which the applicants' claimed invention can be derived, by routine experimentation or otherwise.

The failure of Heitz et al. in this respect is illustrated, by way of example, in the statement at page 8 of the Office Action concerning claim 29, in which the examiner acknowledges that Heitz fails to disclose a GaN material with background impurities including silicon and oxygen, in which iron dopant has a concentration greater than the total concentration of such silicon and oxygen, but then speculates that:

"it would have been obvious that a background impurity of silicon and/or oxygen would render the gallium nitride layer non-insulating and therefore since Heitz teaches a semi-insulating [sic] the silicon and/or oxygen concentration in the background would be obviously low."

Such rejection of claim 29 therefore is based on a hypothetical importation into GaN material of the silicon and oxygen as background impurities, which are conceded to be absent from Heitz et al., but which somehow, despite the absence of any disclosure in Heitz et al., are put into the material and then somehow, again without basis in the reference, compensated by iron doping.

Such rejection thus is based on a string of speculative hypotheses that are fully outside the scope of any reasonable interpretation of the Heitz et al. reference. There is simply no basis on which one of ordinary skill in the art, reading Heitz et al. for what it fairly discloses, would in any straightforward, logical manner extrapolate the compositional teachings of Heitz et al. to include components which are nowhere mentioned in such reference!

Concerning claim 30, the examiner has rejected such claim over Heitz et al. in view of Heikman et al. "Claim 30 recites a doping concentration of iron in a range of from about 3E16 atoms per cubic centimeter to about 7E17 atoms per cubic centimeter, as determined by SIMS. The examiner has contended that it would have been obvious to one of ordinary skill in the art to incorporate the doping range of Heikman et al. into the Heitz et al. "semiconductor device."

Heikman et al. contains no teaching or suggestion of a large-area, single-crystal GaN. It again is pointed out that the term "large area" requires a transverse dimension (diameter or diagonal, depending on the shape of the wafer; see paragraph [0022] of the present application) of at least 25 mm. The single transverse dimension disclosed in Heikman et al. is the reference dimension of 100 micrometers associated with the dimensional bar in Fig. 2 of the article. Such reference dimension, as

applied to the overall height and width of the micrograph, indicates that each of the height and width dimensions of the illustrated section of GaN in Fig. 2 of the Heikman et al. is between 200 and 300 micrometers. The minimum transverse dimension required by applicants' claims (25 mm) is 25,000 micrometers, many orders of magnitude above the dimension illustrated in Fig. 2 of Heikman et al.

Further, the surface of the GaN material shown in Fig. 2 of Heikman et al. is highly cracked and discontinuous in character across the full extent of such surface, and such surface in fact is described by Heikman et al. as a "rough surface." This surface of Heikman et al.'s material does not give any indication of being a single crystal material - the extremely rough, cracked, discontinuous character of the material in fact appears more consistent with a polycrystalline or even semi-amorphous material. There is no mention in Heitz et al. of any "single crystal" material.

Still further, the film thickness disclosed in Heikman et al. for the GaN material of such reference is 2.6 micrometers, of which only the first 0.3 µm of the film is doped. See the abstract of Heikman et al. See also the Fig. 3 SIMS profile in Heikman et al., which shows a film depth of 1 µm. This is to be contrasted with the disclosure of the applicants' GaN material in paragraph [0022] of the present application, where it is stated that "[T]he thickness dimension [of applicants' GaN material] desirably is at least 300 micrometers." This 300<sup>+</sup> micrometers thickness is claimed in a number of the pending claims of the present application. Again, such dimensional characteristic of applicants' GaN material is orders of magnitude above the thickness disclosed in Heikman et al.

Moreover, the resistivity values given in Heikman et al. are sheet resistivities, and not bulk resistivities; there is no basis for viewing Heikman et al. as in any way describing or suggesting applicants' claimed GaN material.

The foregoing reflects the fact that the disclosure in Heikman et al. is fundamentally deficient in relation to the disclosure of GaN material in Heitz et al., and there is nothing in their combination that would yield, motivate or extrapolate to applicants' claimed GaN material. The combination of Heitz et al. and Heikman et al. does not yield large-area GaN, and it does not yield a single-crystal GaN.

Concerning claims 14, 39, 40 and 41, the same have been rejected as unpatentable over Heitz et al. in view of Cuomo et al., on the stated basis that "it would have been obvious to one of ordinary skill in

the art to incorporate the limitations of Cuomo into the Heitz semiconductor device, because bulk materials can be used as substrates upon which microelectronic and optical devices are fabricated."

The Cuomo reference teaches use of  $M^{III}N$  columns for growth of continuous, low defect-density GaN layer that are n-type or p-type (see Cuomo et al., at column 14, lines 22-27), but it does not in any way way teach or suggest the formation of semi-insulating gallium nitride, as required by the claims of the present application. Cuomo et al. at column 14, lines 22-27 merely teaches that:

"During their respective growth steps, columns 14, CEO layer 20, and/or bulk  $M^{III}N$  layer 30 can be doped by conducting conventional doping methods, such as by introducing dopant-containing gases into the reaction chamber under controlled conditions. Multiple or alternating layers of dopants can be added to form electronic devices or components such as, for example, p-n junctions." Cuomo et al., column 14, lines 22-27

Thus, the only teaching of Cuomo et al. involving doping is a teaching to form PN junctions. PN junctions comprise p-type semiconductors, i.e., semiconductors in which the concentration of holes is much higher than the concentration of electrons, and n-type semiconductors, i.e., semiconductors in which the concentration of electrons is much higher than the concentration of holes. Neither of such types of semiconductor is semi-insulative in character. There is thus an absence of any mention of, or suggestive basis for, semi-insulative material in the disclosure of Cuomo et al.

Accordingly, not only does Cuomo et al. fail to remedy the multiple deficiencies of Heitz et al., Cuomo et al. in fact teaches away from any provision of semi-insulative material, since the teachings of Cuomo et al. are directed to doped material that is either p-type semiconductor or n-type semiconductor, and there is not even a hint of semi-insulative material anywhere in the disclosure of Cuomo et al.

Further, there is no motivation for combining the disparate disclosures of Heitz et al. and Cuomo et al., since Heitz et al. is simply concerned with photoluminescence studies ("[I]n this paper we report a comprehensive PLE investigation"-Heitz et al., at page 4382) and is fundamentally deficient in providing any basis for applicants' claimed invention. Cuomo et al. is concerned with columnar growth of Group III-nitride, as a platform for growth of bulk materials, and such reference is wholly devoid of any teaching, suggestion or other basis for semi-insulative GaN material.

In consequence, the hypothetical combination of the Heitz et al. and Cuorno et al. references that has been suggested in the Office Action derives solely from an improper hindsight attempt to reconstruct the present invention, and therefore does not provide a proper basis for contending that the subject matter of claims 14 and 39-41 is obvious.

Based on all of the foregoing, the applicants respectfully request the Examiner to reconsider, and upon reconsideration to withdraw, the rejections of claims 1-41.

If any issues remain outstanding, incident to the formal allowance of the application, the Examiner is requested to contact the undersigned attorney at (919) 419-9350 to discuss same, in order that this application may be allowed and passed to issue at an early date.

Respectfully submitted,



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